

**S/N 10/027,237**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Oleg Zaboronski et al.

Examiner: Tan V Mai

Serial No.: 10/027,237

Group Art Unit: 2193

Filed: December 20, 2001

Docket No.: 1365.059US1

Title: LOGIC CIRCUITS FOR PERFORMING MODULAR MULTIPLICATION  
AND EXPONENTIATION

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**AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

This responds to the Office Action mailed on May 26, 2006. Please amend the above-identified patent application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a three month extension of the period for responding to the Office Action, thereby moving the deadline for response from August 26, 2006 to November 27, 2006 (Monday).

